



SM535-1 ADSL Line Transformers



Features

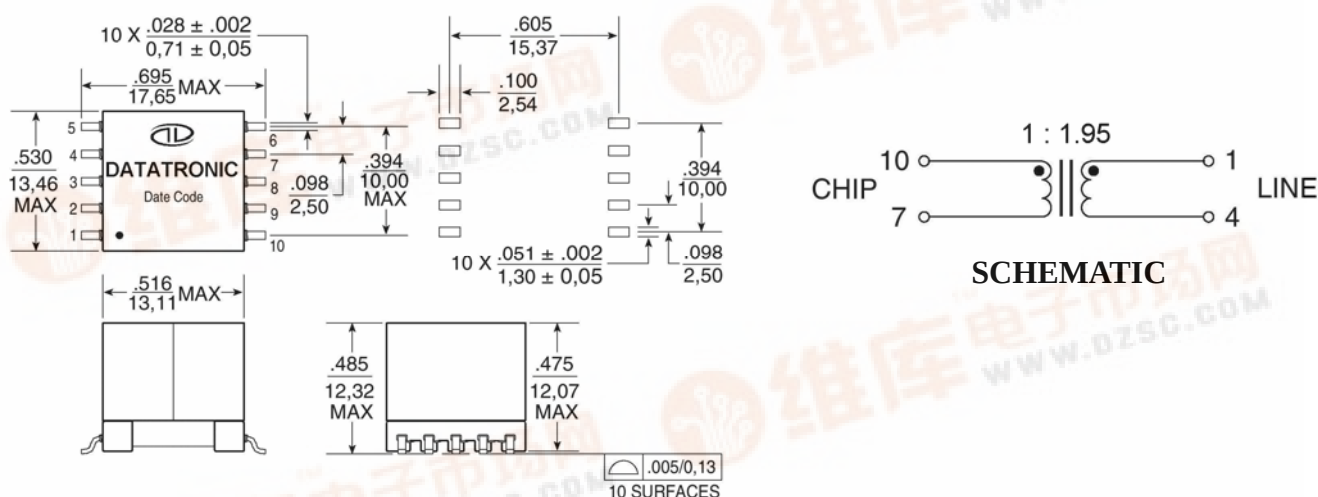
- For use with Texas Instrument's TNET D 4000 C/P chipset
- Excellent Total Harmonic Distortion (THD) Performance
- SMT and Small size package
- Designed to meet IEC1950

ELECTRICAL SPECIFICATIONS AT 25°C:

OPERATING TEMP: -40°C to +85°C

Turns Ratio (10-7):(1-4)	1 : 1.95±2% (Chip to Line)
OCL @10kHz, 0.1V (1-4) with (2-3) Shorted	1.5mH±10%
Leakage Inductance @10kHz 0.1V (1-4) with (10-7) Shorted	12.0µH MAX
Interwinding Capacitance @10kHz, 0.1V (1-10)	35pF MAX
Isolation Voltage (MAX)	1500 Vrms
DC Resistance (1-4) with (2-3) Shorted	2.0Ω MAX
Application	CO

OUTLINE DIMENSIONS



NOTES:

1. Dimensions are shown in inches/millimeters
Unless otherwise specified, tolerance = Inches: ± 0.010
= Millimeters: ± 0.25
2. For non-RoHS parts replace SM prefix with 42- (e.g. SM535-1 becomes 42-535-1)
3. Terminal finish is compliant to RoHS requirements. Solder in accordance with J-STD-020C.



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