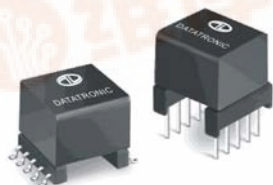




SM538-1 and PT539-1 ADSL Line Transformers



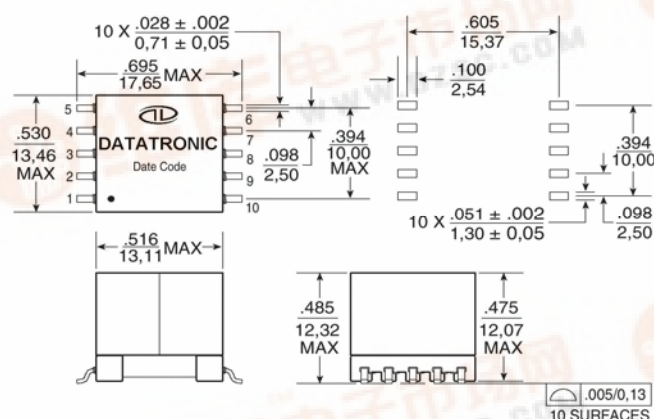
Features

- For use with Analog Devices AD20msp910 / AD20msp918 ADSL Chipsets
- Excellent Total Harmonic Distortion (THD) performance in a small size
- Both SMT and Thru-Hole package version
- Designed to meet UL1950 and EN60950 supplementary insulation requirements for voltages up to 250Vrms maximum

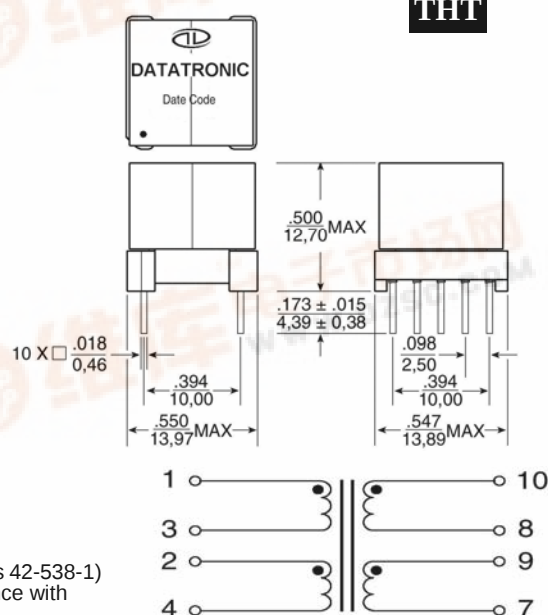
ELECTRICAL SPECIFICATIONS AT 25°C OPERATING TEMP -40°C TO 85°C

	SM538-1	PT539-1
URNS RATIO	1 : 1±2%	1 : 1±2%
OCL @10kHz, 0.1V		
(1-4) WITH (2-3) SHORTED	5.0mH±10%	5.0mH±10%
(10-7) WITH (8-9) SHORTED	5.0mH±10%	5.0mH±10%
LEAKAGE INDUCTANCE @100kHz 0.1V		
(1-4) WITH (2-3), (8-9), (10-7) SHORTED	18.0μH MAX	18.0μH MAX
LONGITUDINAL BALANCE 25kHz – 1.1MHz	40dB MIN	40dB MIN
INSERTION LOSS @100kHz	<0.5dB	<0.5dB
ISOLATION VOLTAGE (MAX)	1500 Vrms	1500 Vrms
LOAD RESISTANCE	100Ω	100Ω
DC RESISTANCE (1-4), (10-7)	3Ω MAX	3Ω MAX
THD LINEARITY @300kHz	-60dB TYP	-
@30kHz	-	-80dB TYP

OUTLINE DIMENSIONS



THT



NOTES:

1. Dimensions are shown in inches/millimeters
Unless otherwise specified, tolerance = Inches: ±0.010
= Millimeters: ±0.25
2. For non-RoHS parts replace SM prefix with 42- (e.g. SM538-1 becomes 42-538-1)
3. Terminal finish is compliant to RoHS requirements. Solder in accordance with J-STD-020C.

DATATRONIC:

28151 Highway 74, Romoland, CA 92858 Tel: 951-928-7700 Toll Free 888-889-5391 Fax: 951-928-7701

Email: ddisales@datatronics.com

499 King's Road, Hong Kong Tel: (852) 2562 3858, (852) 2564 8477 Fax: (852) 2565 7214, (852) 2563 1290

All specifications are subject to change without notice.



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