

ASSP

# Spread Spectrum Clock Generator

## MB88151A

### ■ DESCRIPTION

MB88151A is a clock generator for EMI (Electro Magnetic Interference) reduction. The peak of unnecessary radiation noise (EMI) can be attenuated by making the oscillation frequency slightly modulate periodically with the internal modulator. It corresponds to both of the center spread which modulates frequency in modulation off as Middle Centered and down spread which modulates so as not to exceed frequency in modulation off.

### ■ FEATURES

|                                              | MB88151A-<br>100/101<br>(multiply-by-1)       | MB88151A-<br>200/201<br>(multiply-by-2)       | MB88151A-<br>400/401<br>(multiply-by-4)        | MB88151A-<br>500/501<br>(multiply-by-1/2)    | MB88151A-<br>800/801<br>(multiply-by-8)      |
|----------------------------------------------|-----------------------------------------------|-----------------------------------------------|------------------------------------------------|----------------------------------------------|----------------------------------------------|
| Input<br>frequency/<br>Output<br>frequency   | 16.6 MHz to 33.4 MHz/<br>16.6 MHz to 33.4 MHz | 16.6 MHz to 33.4 MHz/<br>33.2 MHz to 66.8 MHz | 16.6 MHz to 33.4 MHz/<br>66.4 MHz to 133.6 MHz | 16.6 MHz to 33.4 MHz/<br>8.3 MHz to 16.7 MHz | 8.3 MHz to 16.7 MHz<br>66.4 MHz to 133.6 MHz |
| Modulation<br>clock<br>cycle-cycle<br>jitter | Less than 100 ps                              | Less than 100 ps                              | Less than 150 ps                               | Less than 200 ps                             | Less than 150 ps                             |

- Modulation rate :  $\pm 0.5\%$ ,  $\pm 1.5\%$  (Center spread),  $-1.0\%$ ,  $-3.0\%$  (Down spread)
- Equipped with oscillation circuit : Range of oscillation 8.3 MHz to 33.4 MHz
- Modulation clock output Duty : 40% to 60%
- Low current consumption by CMOS process : 5 mA (24 MHz : Typ-sample, no load)
- Power supply voltage :  $3.3\text{ V} \pm 0.3\text{ V}$
- Operating temperature :  $-40\text{ }^{\circ}\text{C}$  to  $+85\text{ }^{\circ}\text{C}$
- Package : SOP 8-pin

# MB88151A

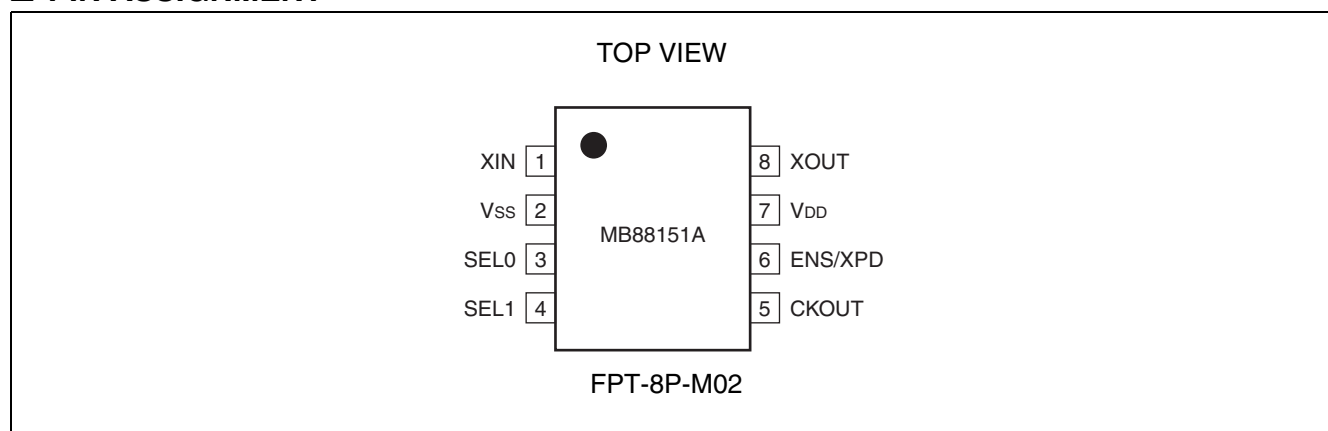
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## ■ PRODUCT LINEUP

MB88151A has five kinds of multiplication type.

| Product          | Input frequency range | Multiplier ratio | Output frequency range |
|------------------|-----------------------|------------------|------------------------|
| MB88151A-100/101 | 16.6 MHz to 33.4 MHz  | Multiply-by-1    | 16.6 MHz to 33.4 MHz   |
| MB88151A-200/201 |                       | Multiply-by-2    | 33.2 MHz to 66.8 MHz   |
| MB88151A-400/401 |                       | Multiply-by-4    | 66.4 MHz to 133.6 MHz  |
| MB88151A-500/501 |                       | Multiply-by-1/2  | 8.3 MHz to 16.7 MHz    |
| MB88151A-800/801 | 8.3 MHz to 16.7 MHz   | Multiply-by-8    | 66.4 MHz to 133.6 MHz  |

## ■ PIN ASSIGNMENT

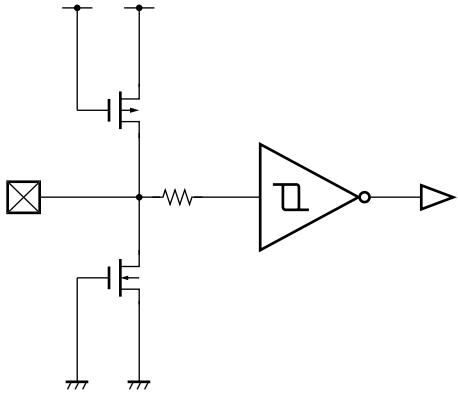
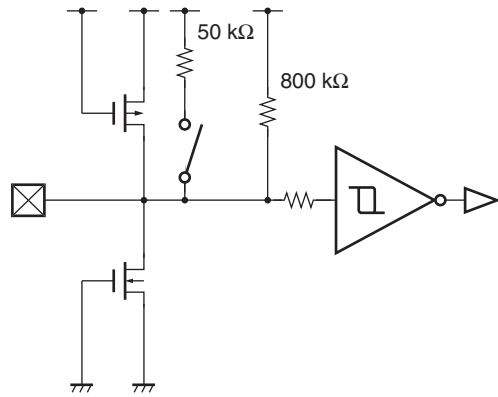
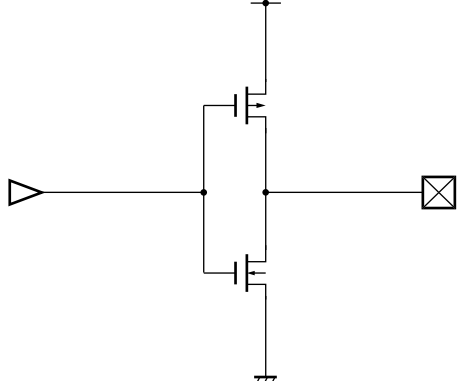


## ■ PIN DESCRIPTION

| Pin name        | I/O | Pin no. | Description                                                                                         |
|-----------------|-----|---------|-----------------------------------------------------------------------------------------------------|
| XIN             | I   | 1       | Resonator connection pin/clock input pin                                                            |
| V <sub>SS</sub> | —   | 2       | GND pin                                                                                             |
| SEL0            | I   | 3       | Modulation rate setting pin                                                                         |
| SEL1            | I   | 4       | Modulation rate setting pin                                                                         |
| CKOUT           | O   | 5       | Modulated clock output pin                                                                          |
| ENS/XPD         | I   | 6       | Modulation enable setting pin (with pull-up resistance)/<br>Power down pin (with pull-up resistor)* |
| V <sub>DD</sub> | —   | 7       | Power supply voltage pin                                                                            |
| XOUT            | O   | 8       | Resonator connection pin                                                                            |

\* : XPD = 800 kΩ pull-up resistor at “L”

## ■ I/O CIRCUIT TYPE

| Pin           | Circuit type                                                                        | Remarks                                                                                                                                                                                  |
|---------------|-------------------------------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| SEL0,<br>SEL1 |    | CMOS hysteresis input                                                                                                                                                                    |
| ENS           |   | <p>CMOS hysteresis input with 50 kΩ + 800 kΩ (Typ) pull-up resistors</p> <p>Note : If “L” is input to XPD when the XPD function is selected, 50 kΩ pull-up resistor is disconnected.</p> |
| CKOUT         |  | <ul style="list-style-type: none"> <li>• CMOS output</li> <li>• <math>I_{OL} = 4 \text{ mA}</math></li> </ul>                                                                            |

Note : For XIN and XOUT pins, refer to “■ OSCILLATION CIRCUIT”.

## ■ HANDLING DEVICES

### Preventing Latch-up

A latch-up can occur if, on this device, (a) a voltage higher than  $V_{DD}$  or a voltage lower than  $V_{SS}$  is applied to an input or output pin or (b) a voltage higher than the rating is applied between  $V_{DD}$  pin and  $V_{SS}$  pin. The latch-up, if it occurs, significantly increases the power supply current and may cause thermal destruction of an element. When you use this device, be very careful not to exceed the maximum rating.

### Handling unused pins

Do not leave an unused input pin open, since it may cause a malfunction. Handle by, using a pull-up or pull-down resistor.

Unused output pin should be opened.

### The attention when the external clock is used

Input the clock to XIN pin, and XOUT pin should be opened when you use the external clock. Please pay attention so that an overshoot and an undershoot do not occur to an input clock of XIN pin.

### Power supply pins

Please design connecting the power supply pin of this device by as low impedance as possible from the current supply source.

We recommend connecting electrolytic capacitor (about 10  $\mu F$ ) and the ceramic capacitor (about 0.01  $\mu F$ ) in parallel between  $V_{SS}$  pin and  $V_{DD}$  pin near the device, as a bypass capacitor.

### Oscillation circuit

Noise near the XIN and XOUT pins may cause the device to malfunction. Design printed circuit boards so that electric wiring of XIN or XOUT pin and the resonator do not intersect other wiring.

Design the printed circuit board that surrounds the XIN and XOUT pins with ground.

The diagram illustrates the MB88151A PLL block, showing both its external pin configuration and its internal functional architecture.

**External Pin Configuration:**

- SEL1, SEL0:** Modulation rate setting inputs.
- ENS/XPD:** Modulation enable setting/Power down setting input.
- XOUT:** Reference clock output pin.
- XIN:** Reference clock input pin.
- CKOUT:** Modulation clock output pin.

**Internal PLL Architecture:**

- The **Reference clock** is divided by  $1/N$  and fed into the **Phase compare** block.
- The **Phase compare** block outputs to the **Charge pump**.
- The **Charge pump** output is filtered by the **Loop filter** (consisting of a **V/I conversion** block and a capacitor).
- The filtered signal is converted to a current by the **IDAC** (glitchless IDAC).
- The **IDAC** output is fed into the **ICO** (modulation clock output).
- The **IDAC** is also controlled by **Modulation logic**, which receives inputs from **SEL1**, **SEL0**, and **ENS/XPD**.
- The **Modulation logic** also controls the **Reference clock** divider ( $1/N$ ) and the **Charge pump**.

**MB88151A PLL block**

A glitchless IDAC (current output D/A converter) provides precise modulation, thereby dramatically reducing EMI.

# MB88151A

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## ■ PIN SETTING

When changing the pin setting, the stabilization wait time for the modulation clock is required. The stabilization wait time for the modulation clock take the maximum value of “■ ELECTRICAL CHARACTERISTICS • AC Characteristics Lock-up time”.

### ENS modulation enable setting (MB88151A-100/200/400/500/800)

| ENS | Modulation    |
|-----|---------------|
| L   | No modulation |
| H   | Modulation    |

Note : Spectrum does not spread when “L” is set to ENS. The clock with low jitter can be obtained. Because of ENS has Pull-up resistance, spectrum spread when “H” is set to it or open the terminal.

### XPD Power down setting (MB88151A-101/201/401/501/801)

| XPD | Status            |
|-----|-------------------|
| L   | Power down Status |
| H   | Operating status  |

Note : CKOUT of output pins are fixed to “L” output during power down.

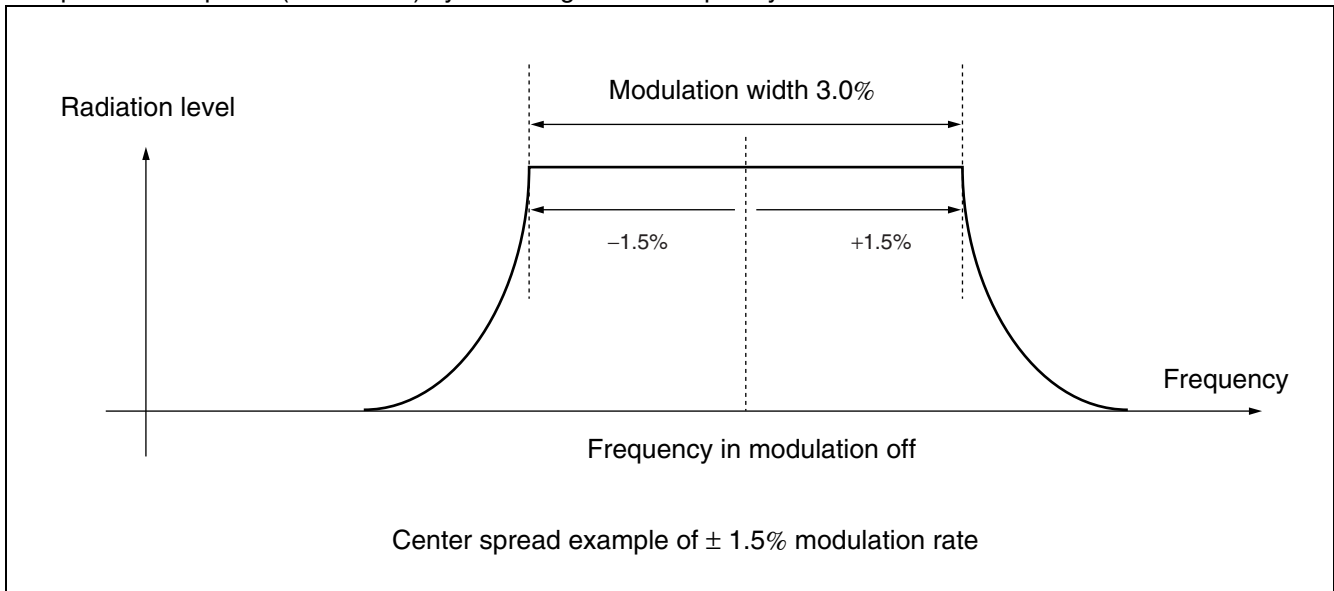
### SEL0, SEL1 Modulation rate setting

| SEL1 | SEL0 | Modulation rate | Modulation type |
|------|------|-----------------|-----------------|
| L    | L    | $\pm 1.5\%$     | Center spread   |
| L    | H    | $\pm 0.5\%$     | Center spread   |
| H    | L    | $- 1.0\%$       | Down spread     |
| H    | H    | $- 3.0\%$       | Down spread     |

Note : The modulation rate can be changed at the level of the terminal.

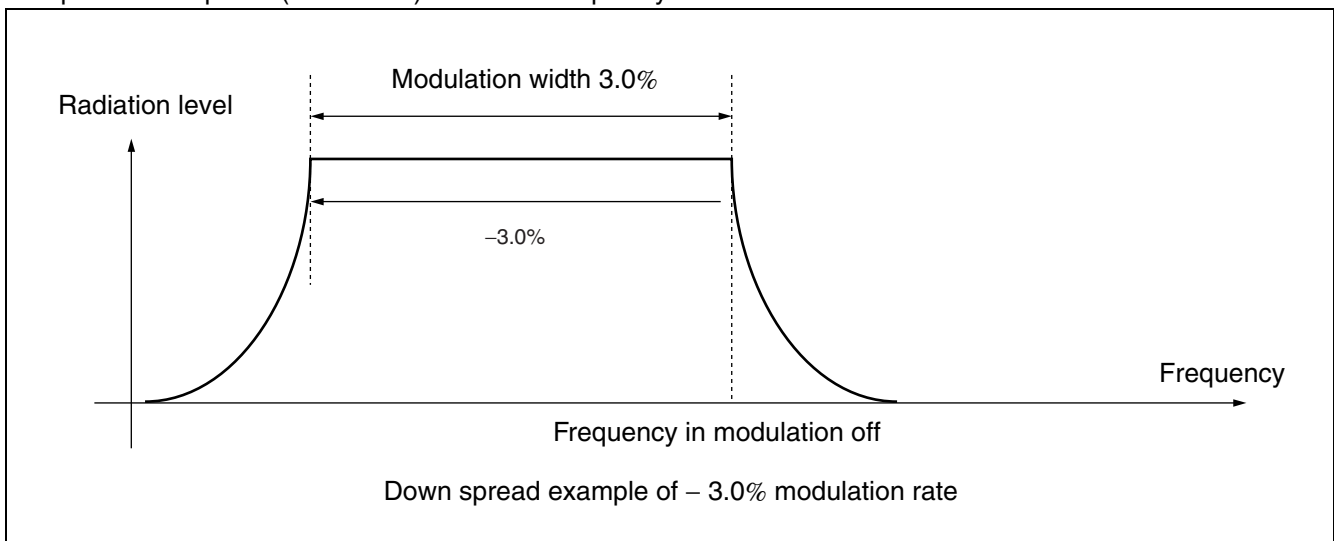
- Center spread

Spectrum is spread (modulated) by centering on the frequency in modulation off.



- Down spread

Spectrum is spread (modulated) below the frequency in modulation off.



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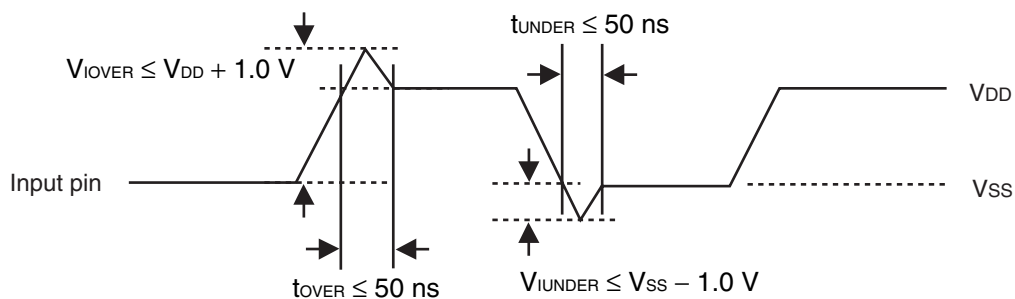
## ■ ABSOLUTE MAXIMUM RATINGS

| Parameter                      | Symbol       | Rating                                   |                                         | Unit |
|--------------------------------|--------------|------------------------------------------|-----------------------------------------|------|
|                                |              | Min                                      | Max                                     |      |
| Power supply voltage*          | $V_{DD}$     | - 0.5                                    | + 4.0                                   | V    |
| Input voltage*                 | $V_I$        | $V_{SS} - 0.5$                           | $V_{DD} + 0.5$                          | V    |
| Output voltage*                | $V_O$        | $V_{SS} - 0.5$                           | $V_{DD} + 0.5$                          | V    |
| Storage temperature            | $T_{ST}$     | - 55                                     | + 125                                   | °C   |
| Operation junction temperature | $T_J$        | - 40                                     | + 125                                   | °C   |
| Output current                 | $I_O$        | - 14                                     | + 14                                    | mA   |
| Overshoot                      | $V_{IOVER}$  | —                                        | $V_{DD} + 1.0$ ( $t_{OVER} \leq 50$ ns) | V    |
| Undershoot                     | $V_{IUNDER}$ | $V_{SS} - 1.0$ ( $t_{UNDER} \leq 50$ ns) | —                                       | V    |

\* : The parameter is based on  $V_{SS} = 0.0$  V.

WARNING: Semiconductor devices can be permanently damaged by application of stress (voltage, current, temperature, etc.) in excess of absolute maximum ratings. Do not exceed these ratings.

### Overshoot/Undershoot





## ■ RECOMMENDED OPERATING CONDITIONS

( $V_{SS} = 0.0\text{ V}$ )

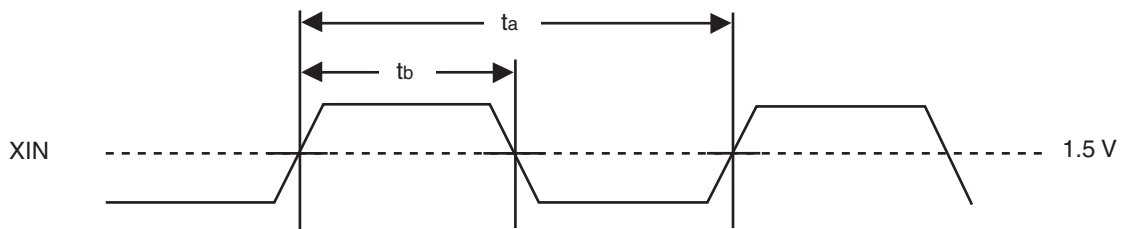
| Parameter               | Symbol    | Pin                           | Conditions          | Value               |     |                     | Unit |
|-------------------------|-----------|-------------------------------|---------------------|---------------------|-----|---------------------|------|
|                         |           |                               |                     | Min                 | Typ | Max                 |      |
| Power supply voltage    | $V_{DD}$  | $V_{DD}$                      | —                   | 3.0                 | 3.3 | 3.6                 | V    |
| “H” level input voltage | $V_{IH}$  | XIN,<br>SEL0,<br>SEL1,<br>ENS | —                   | $V_{DD} \times 0.8$ | —   | $V_{DD} + 0.3$      | V    |
| “L” level input voltage | $V_{IL}$  |                               | —                   | $V_{SS}$            | —   | $V_{DD} \times 0.2$ | V    |
| Input clock duty cycle  | $t_{DCI}$ | XIN                           | 8.3 MHz to 33.4 MHz | 40                  | 50  | 60                  | %    |
| Operating temperature   | $T_a$     | —                             | —                   | − 40                | —   | + 85                | °C   |

**WARNING:** The recommended operating conditions are required in order to ensure the normal operation of the semiconductor device. All of the device's electrical characteristics are warranted when the device is operated within these ranges.

Always use semiconductor devices within their recommended operating condition ranges. Operation outside these ranges may adversely affect reliability and could result in device failure.

No warranty is made with respect to uses, operating conditions, or combinations not represented on the data sheet. Users considering application outside the listed conditions are advised to contact their FUJITSU representatives beforehand.

Input clock duty cycle ( $t_{DCI} = t_b/t_a$ )



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## ■ ELECTRICAL CHARACTERISTICS

- DC Characteristics

(Ta = - 40 °C to + 85 °C, V<sub>DD</sub> = 3.3 V ± 0.3 V, V<sub>SS</sub> = 0.0 V)

| Parameter                | Symbol           | Pin                           | Conditions                                                              | Value                 |     |                 | Unit |
|--------------------------|------------------|-------------------------------|-------------------------------------------------------------------------|-----------------------|-----|-----------------|------|
|                          |                  |                               |                                                                         | Min                   | Typ | Max             |      |
| Power supply current     | I <sub>CC</sub>  | V <sub>DD</sub>               | No load capacitance at output 24 MHz<br>MB88151A-100                    | —                     | 5.0 | 7.0             | mA   |
|                          |                  |                               | At power down<br>MB88151A-101                                           | —                     | 10  | —               | μA   |
| Output voltage           | V <sub>OH</sub>  | CKOUT                         | “H” level output,<br>I <sub>OH</sub> = - 4 mA                           | V <sub>DD</sub> - 0.5 | —   | V <sub>DD</sub> | V    |
|                          | V <sub>OL</sub>  |                               | “L” level output,<br>I <sub>OL</sub> = 4 mA                             | V <sub>SS</sub>       | —   | 0.4             | V    |
| Output impedance         | Z <sub>O</sub>   | CKOUT                         | 8.3 MHz to 133.6 MHz                                                    | —                     | 45  | —               | Ω    |
| Input capacitance        | C <sub>IN</sub>  | XIN,<br>SEL0,<br>SEL1,<br>ENS | Ta = + 25 °C,<br>V <sub>DD</sub> = V <sub>I</sub> = 0.0 V,<br>f = 1 MHz | —                     | —   | 16              | pF   |
| Load capacitance         | C <sub>L</sub>   | CKOUT                         | 8.3 MHz to 66.8 MHz                                                     | —                     | —   | 15              | pF   |
|                          |                  |                               | 66.8 MHz to 100 MHz                                                     | —                     | —   | 10              |      |
|                          |                  |                               | 100 MHz to 133.6 MHz                                                    | —                     | —   | 7               |      |
| Input pull-up resistance | R <sub>PUE</sub> | ENS                           | V <sub>IL</sub> = 0.0 V                                                 | 25                    | 50  | 200             | kΩ   |
|                          | R <sub>PUP</sub> | XPD                           | V <sub>IL</sub> = 0.0 V                                                 | 500                   | 800 | 1200            |      |

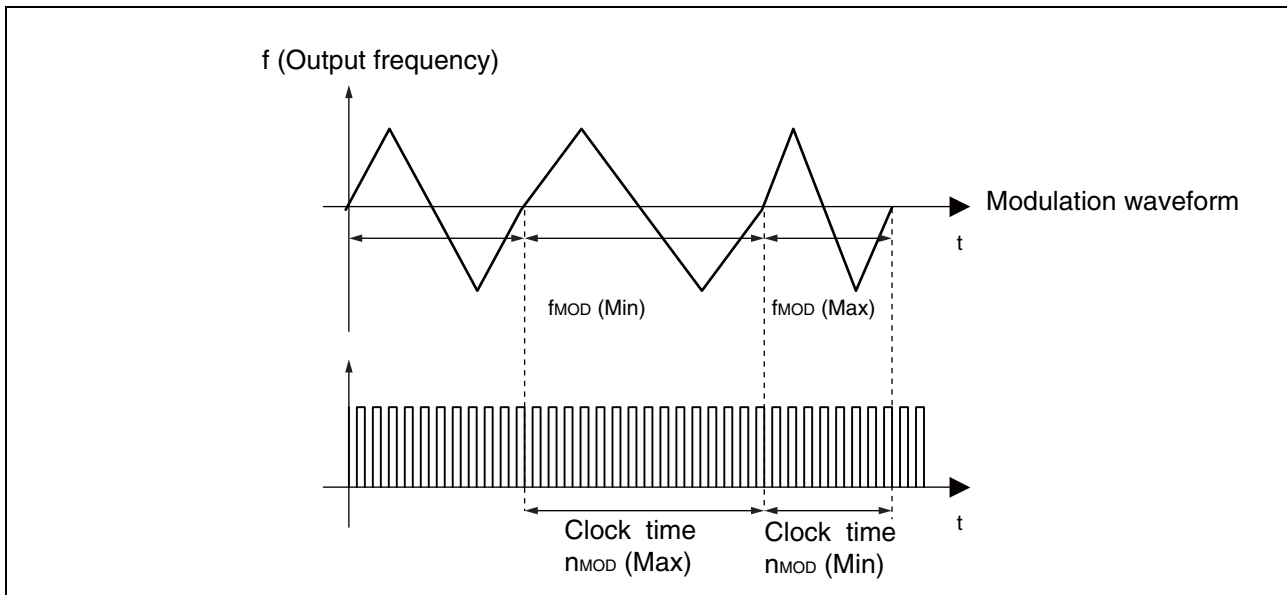
• AC Characteristics

(Ta = -40 °C to +85 °C, V<sub>DD</sub> = 3.3 V ± 0.3 V, V<sub>SS</sub> = 0.0 V)

| Parameter                                                    | Symbol                                  | Pin          | Conditions                                                                                            | Value                           |                                 |                                 | Unit          |
|--------------------------------------------------------------|-----------------------------------------|--------------|-------------------------------------------------------------------------------------------------------|---------------------------------|---------------------------------|---------------------------------|---------------|
|                                                              |                                         |              |                                                                                                       | Min                             | Typ                             | Max                             |               |
| Oscillation frequency                                        | f <sub>x</sub>                          | XIN,<br>XOUT | Fundamental oscillation                                                                               | 8.3                             | —                               | 33.4                            | MHz           |
| Input frequency                                              | f <sub>in</sub>                         | XIN          | External clock input (multiply-by-1, 2, 4, divided by 2)                                              | 16.6                            | —                               | 33.4                            | MHz           |
|                                                              |                                         |              | External clock input (multiply-by-8)                                                                  | 8.3                             | —                               | 16.7                            |               |
| Output frequency                                             | f <sub>OUT</sub>                        | CKOUT        | MB88151A-100/101 (Multiply by 1)                                                                      | 16.6                            | —                               | 33.4                            | MHz           |
|                                                              |                                         |              | MB88151A-200/201 (Multiply by 2)                                                                      | 33.2                            | —                               | 66.8                            |               |
|                                                              |                                         |              | MB88151A-400/401 (Multiply by 4)                                                                      | 66.4                            | —                               | 133.6                           |               |
|                                                              |                                         |              | MB88151A-500/501 (2-frequency division)                                                               | 8.3                             | —                               | 16.7                            |               |
|                                                              |                                         |              | MB88151A-800/801 (multiply-by-8)                                                                      | 66.4                            | —                               | 133.6                           |               |
| Output slew rate                                             | SR                                      | CKOUT        | 0.4 V to 2.4 V<br>Load capacitance 15 pF                                                              | 0.4                             | —                               | 4.0                             | V/ns          |
| Output clock duty cycle                                      | t <sub>DCC</sub>                        | CKOUT        | 1.5 V                                                                                                 | 40                              | —                               | 60                              | %             |
| Modulation period<br>(Number of input clocks per modulation) | f <sub>MOD</sub><br>(n <sub>MOD</sub> ) | CKOUT        | MB88151A-100/101,<br>MB88151A-200/201,<br>MB88151A-400/401,<br>MB88151A-500/501                       | f <sub>in</sub> /2200<br>(2200) | f <sub>in</sub> /1900<br>(1900) | f <sub>in</sub> /1600<br>(1600) | kHz<br>(clks) |
|                                                              |                                         |              | MB88151A-800/801                                                                                      | f <sub>in</sub> /880<br>(880)   | f <sub>in</sub> /760<br>(760)   | f <sub>in</sub> /640<br>(640)   | kHz<br>(clks) |
| Lock-up time                                                 | t <sub>LK</sub>                         | CKOUT        | 8.3 MHz to 80 MHz                                                                                     | —                               | 2                               | 5                               | ms            |
|                                                              |                                         |              | 80 MHz to 133.6 MHz                                                                                   | —                               | 3                               | 8                               |               |
| Cycle-cycle jitter                                           | t <sub>JC</sub>                         | CKOUT        | MB88151A-100/101,<br>MB88151A-200/201<br>No load capacitance,<br>Ta = +25 °C, V <sub>DD</sub> = 3.3 V | —                               | —                               | 100                             | ps-rms        |
|                                                              |                                         |              | MB88151A-400/401,<br>MB88151A-800/801<br>No load capacitance,<br>Ta = +25 °C, V <sub>DD</sub> = 3.3 V | —                               | —                               | 150                             |               |
|                                                              |                                         |              | MB88151A-500/501<br>No load capacitance,<br>Ta = +25 °C, V <sub>DD</sub> = 3.3 V                      | —                               | —                               | 200                             |               |

Note : The modulation clock stabilization wait time is required after the power is turned on, the IC recovers from power saving, or after FREQ (frequency range) or ENS (modulation ON/OFF) setting is changed. For the modulation clock stabilization wait time, assign the maximum value for lock-up time.

## <Definition of modulation frequency and number of input clocks per modulation>

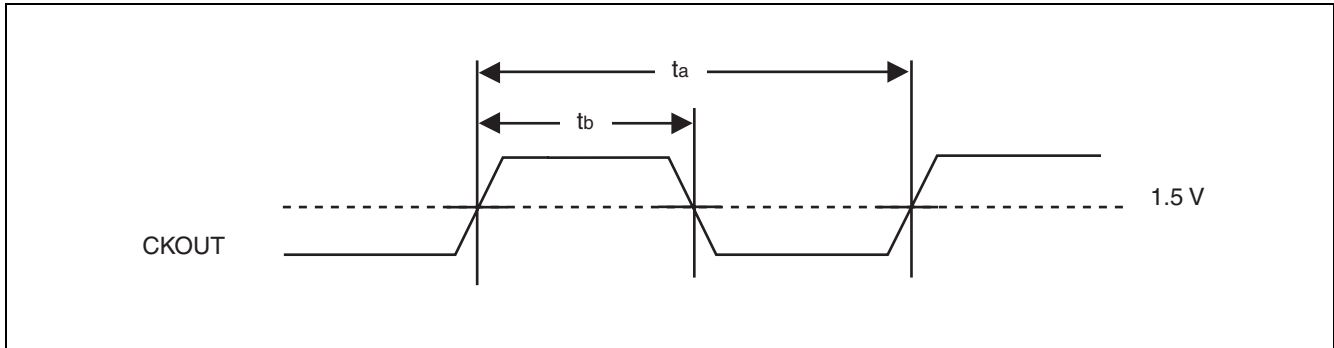


MB88151A contains the modulation period to realize the efficient EMI reduction.

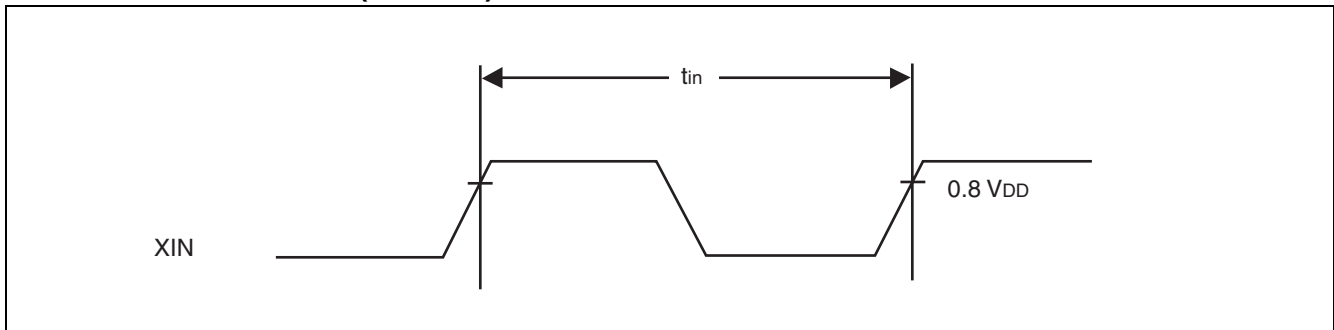
The modulation period  $f_{MOD}$  depends on the input frequency and changes between  $f_{MOD} (Min)$  and  $f_{MOD} (Max)$ .

Furthermore, the average value of  $f_{MOD}$  equals the typical value of the electrical characteristics.

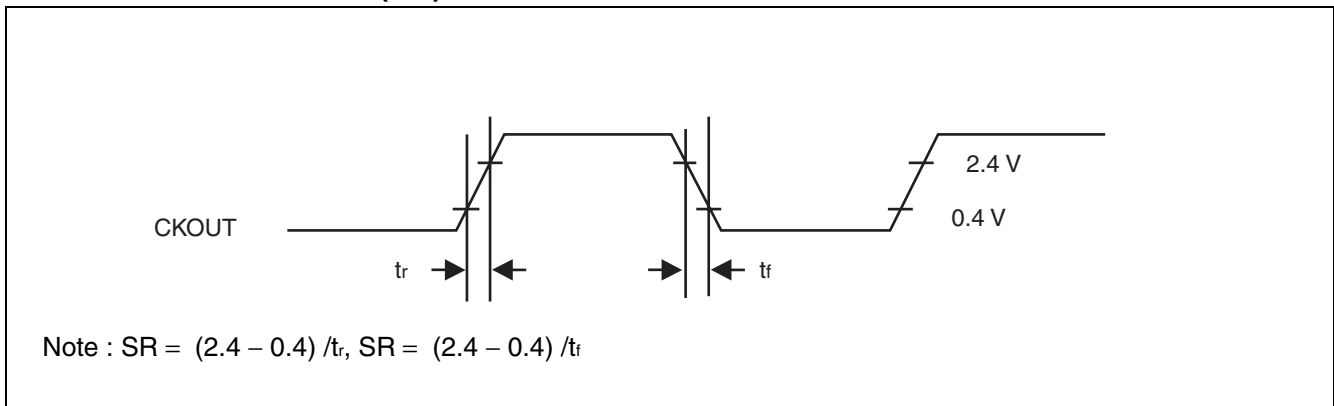
## ■ OUTPUT CLOCK DUTY CYCLE ( $t_{DCC} = t_b/t_a$ )



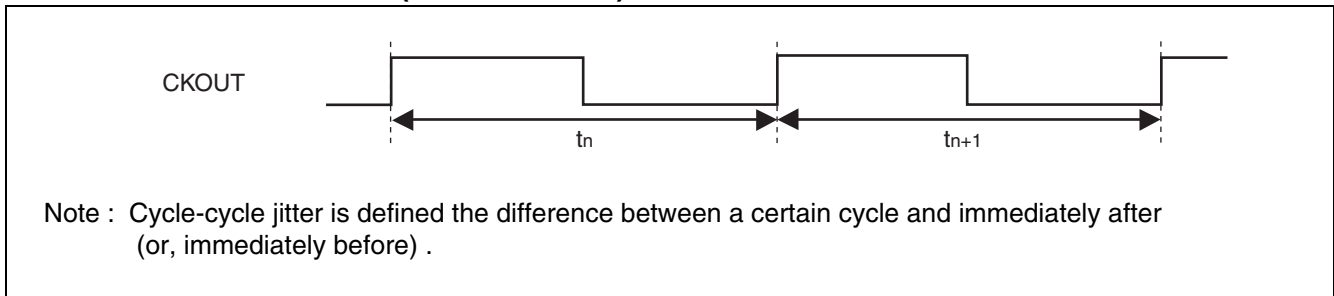
## ■ INPUT FREQUENCY ( $f_{in} = 1/t_{in}$ )



## ■ OUTPUT SLEW RATE (SR)

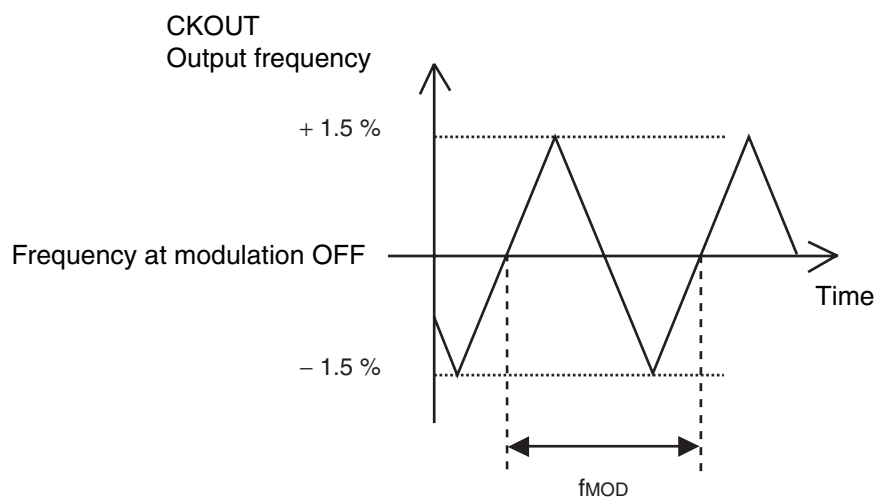


## ■ CYCLE-CYCLE JITTER ( $t_{JC} = |t_n - t_{n+1}|$ )

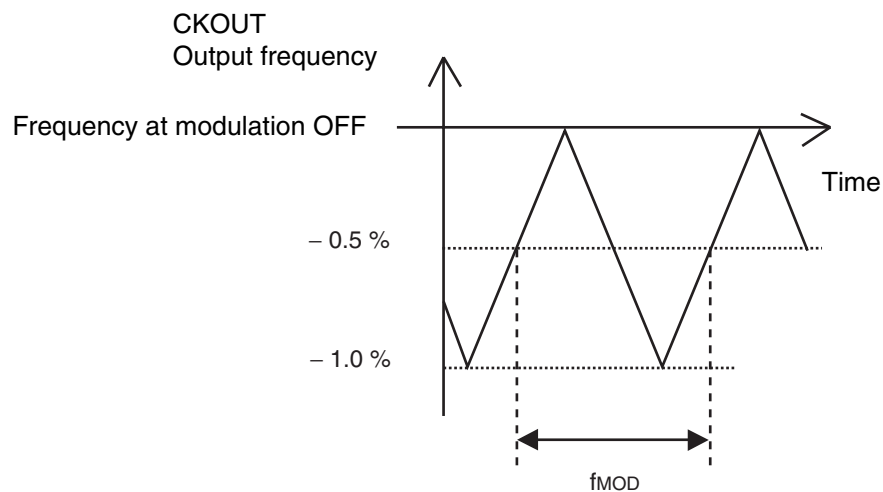


## ■ MODULATION WAVEFORM

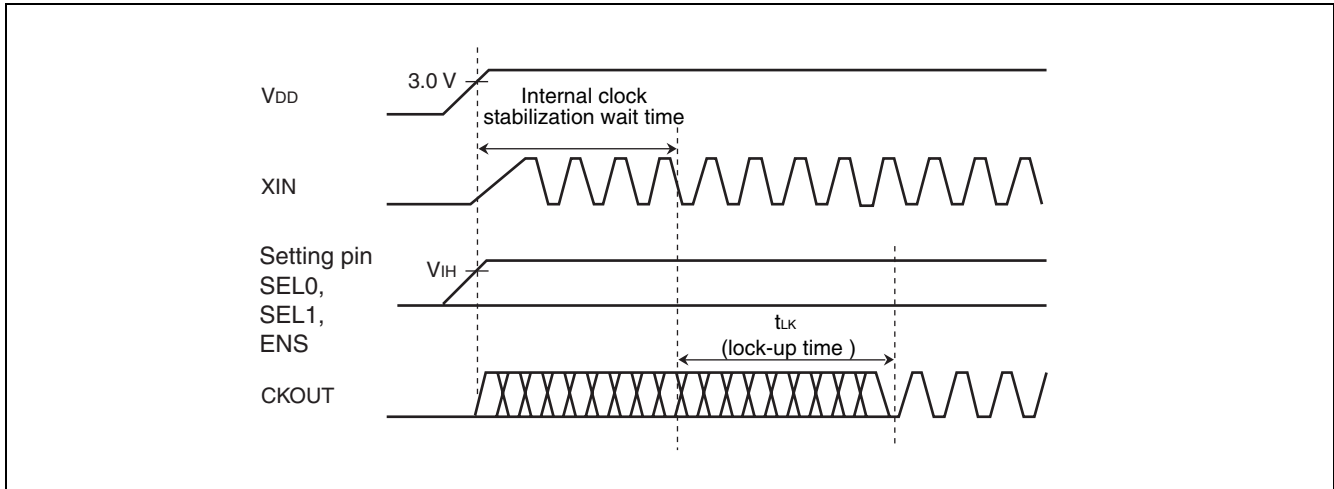
- $\pm 1.5\%$  modulation rate, Example of center spread



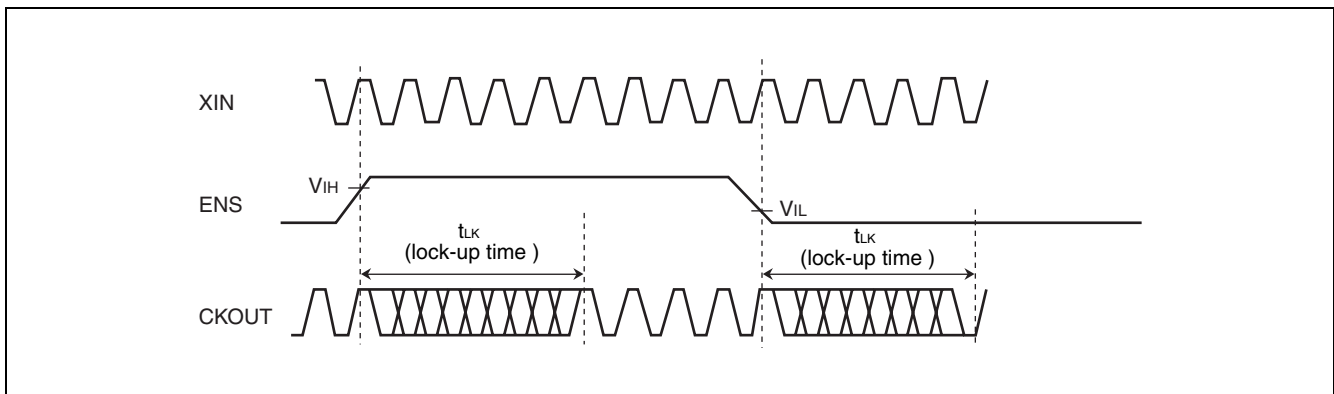
- $-1.0\%$  modulation rate, Example of down spread



## ■ LOCK-UP TIME



If the setting pin is fixed at the “H” or “L” level, the maximum time after the power is turned on until the set clock signal is output from CKOUT pin is (the stabilization wait time of input clock to XIN pin) + (the lock-up time “ $t_{LK}$ ”). For the input clock stabilization time, check the characteristics of the resonator or oscillator used.

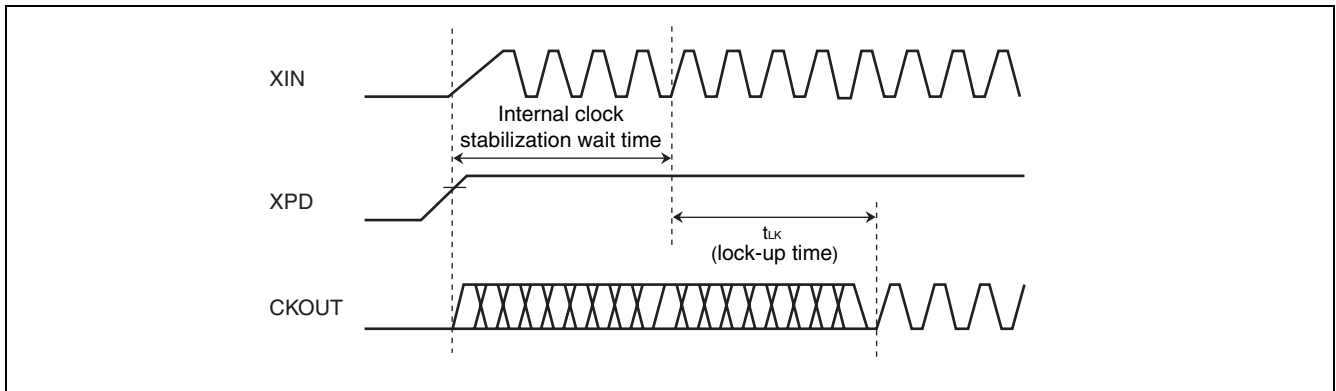


For modulation enable control using the ENS pin during normal operation, the set clock signal is output from CKOUT pin at most the lock-up time ( $t_{LK}$ ) after the level at the ENS pin is determined.

Note : When the pin setting is changed, the CKOUT pin output clock stabilization time is required. Until the output clock signal becomes stable, the output frequency, output clock duty cycle, modulation period, and cycle-cycle jitter cannot be guaranteed. It is therefore advisable to perform processing such as cancelling a reset of the device at the succeeding stage after the lock-up time.

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When the power down is controlled by XPD pin, the desired clock is obtained after the pin is set to H level until the maximum lock-up time  $t_{LK}$  is elapsed.



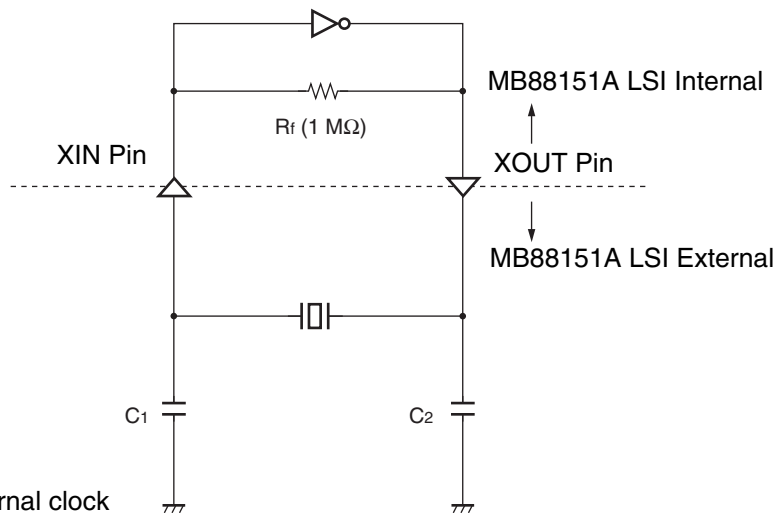
## ■ OSCILLATION CIRCUIT

The figure below shows the connection example about general resonator. The oscillation circuit has the built-in resistance ( $1\text{ M}\Omega$ ). The value of capacity ( $C_1$  and  $C_2$ ) is required adjusting to the most suitable value of individual resonator.

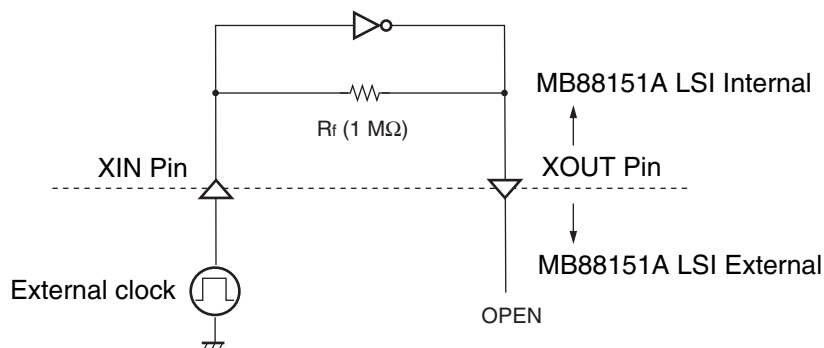
The most suitable value is different by individual resonator. Please refer to the resonator manufacturer which you use for the most suitable value.

Input the clock to XIN pin, and do not connect anything with XOUT pin if you use the external clock (you do not use the resonator).

- When using the resonator



- When using an external clock

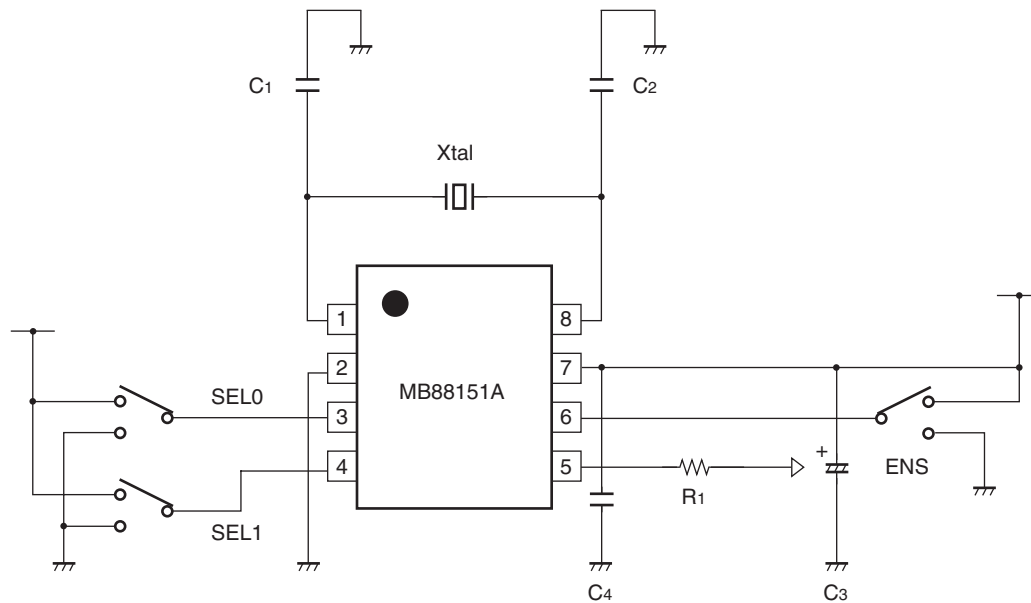


Note : Note that a jitter characteristic of an input clock may cause an affect a cycle-cycle jitter characteristic.

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## ■ INTERCONNECTION CIRCUIT EXAMPLE

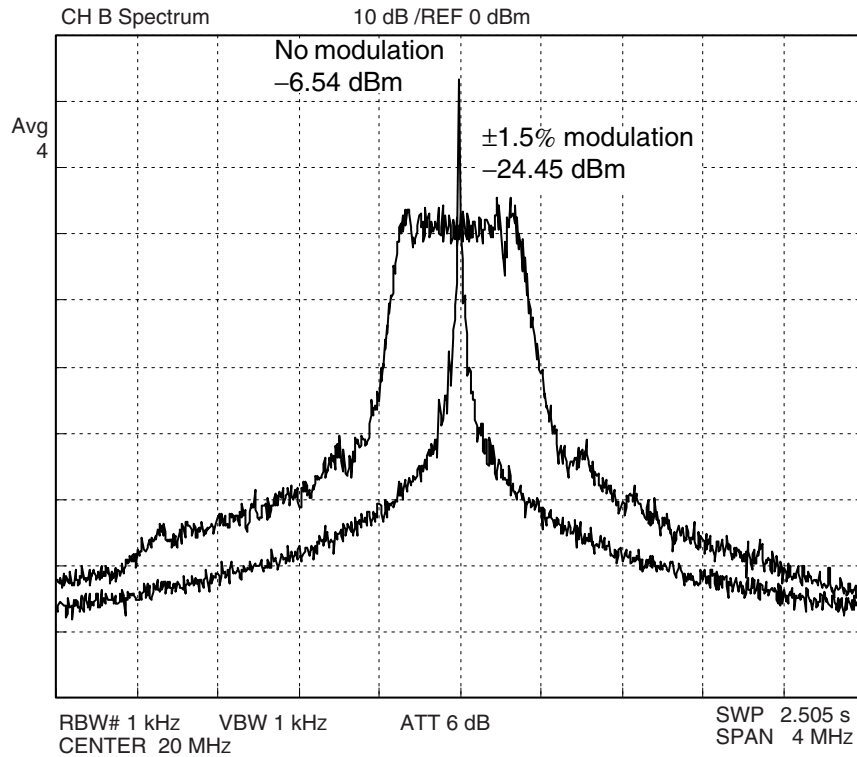


- C<sub>1</sub>, C<sub>2</sub> : Oscillation stabilization capacitance (refer to "■ OSCILLATION CIRCUIT".)  
C<sub>3</sub> : Capacitor of 10  $\mu$ F or higher  
C<sub>4</sub> : Capacitor about 0.01  $\mu$ F (connect a capacitor of good high frequency property (ex. laminated ceramic capacitor) to close to this device.)  
R<sub>1</sub> : Impedance matching resistor for board pattern

## ■ SPECTRUM EXAMPLE CHARACTERISTICS

The condition of the examples of the characteristic is shown as follows : Input frequency = 20 MHz (Output frequency = 20 MHz : Using MB88151A-100 (Multiply-by-1)), Power - supply voltage = 3.3 V, None load capacity, Modulation rate =  $\pm 1.5\%$  (center spread).

Spectrum analyzer HP4396B is connected with CKOUT. The result of the measurement with RBW = 1 kHz (ATT use for - 6dB).



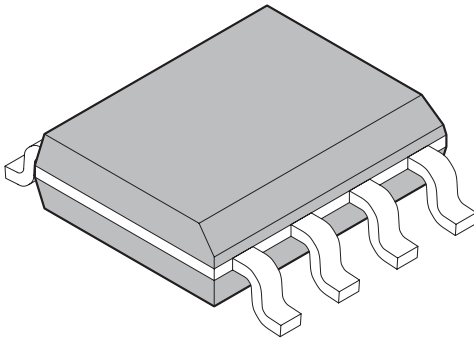
# MB88151A

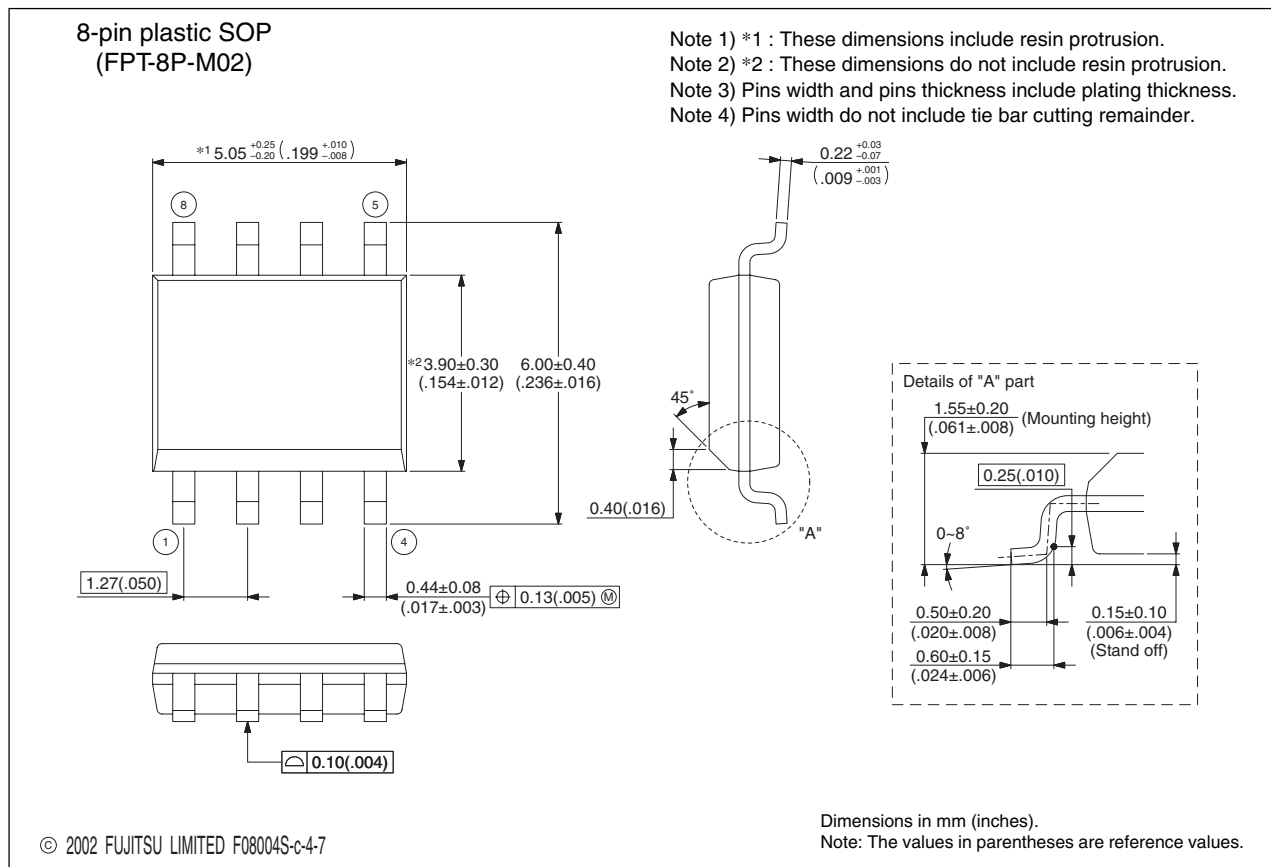
[查询“MB88151A”供应商](#)

## ■ ORDERING INFORMATION

| Part number                                          | Input frequency range | Multiplier ratio | Output frequency range | Package                        | Remarks                  |
|------------------------------------------------------|-----------------------|------------------|------------------------|--------------------------------|--------------------------|
| MB88151APNF-G-100-JNE1<br>MB88151APNF-G-101-JNE1     | 16.6 MHz to 33.4 MHz  | Multiply-by-1    | 16.6 MHz to 33.4 MHz   | 8-pin plastic SOP (FPT-8P-M02) |                          |
| MB88151APNF-G-200-JNE1<br>MB88151APNF-G-201-JNE1     |                       | Multiply-by-2    | 33.2 MHz to 66.8 MHz   |                                |                          |
| MB88151APNF-G-400-JNE1<br>MB88151APNF-G-401-JNE1     |                       | Multiply-by-4    | 66.4 MHz to 133.6 MHz  |                                |                          |
| MB88151APNF-G-500-JNE1<br>MB88151APNF-G-501-JNE1     |                       | Multiply-by-1/2  | 8.3 MHz to 16.7 MHz    |                                |                          |
| MB88151APNF-G-800-JNE1<br>MB88151APNF-G-801-JNE1     | 8.3 MHz to 16.7 MHz   | Multiply-by-8    | 66.4 MHz to 133.6 MHz  |                                | Emboss tapping (EF type) |
| MB88151APNF-G-100-JNEFE1<br>MB88151APNF-G-101-JNEFE1 | 16.6 MHz to 33.4 MHz  | Multiply-by-1    | 16.6 MHz to 33.4 MHz   |                                |                          |
| MB88151APNF-G-200-JNEFE1<br>MB88151APNF-G-201-JNEFE1 |                       | Multiply-by-2    | 33.2 MHz to 66.8 MHz   |                                |                          |
| MB88151APNF-G-400-JNEFE1<br>MB88151APNF-G-401-JNEFE1 |                       | Multiply-by-4    | 66.4 MHz to 133.6 MHz  |                                |                          |
| MB88151APNF-G-500-JNEFE1<br>MB88151APNF-G-501-JNEFE1 |                       | Multiply-by-1/2  | 8.3 MHz to 16.7 MHz    |                                |                          |
| MB88151APNF-G-800-JNEFE1<br>MB88151APNF-G-801-JNEFE1 | 8.3 MHz to 16.7 MHz   | Multiply-by-8    | 66.4 MHz to 133.6 MHz  |                                | Emboss tapping (ER type) |
| MB88151APNF-G-100-JNERE1<br>MB88151APNF-G-101-JNERE1 | 16.6 MHz to 33.4 MHz  | Multiply-by-1    | 16.6 MHz to 33.4 MHz   |                                |                          |
| MB88151APNF-G-200-JNERE1<br>MB88151APNF-G-201-JNERE1 |                       | Multiply-by-2    | 33.2 MHz to 66.8 MHz   |                                |                          |
| MB88151APNF-G-400-JNERE1<br>MB88151APNF-G-401-JNERE1 |                       | Multiply-by-4    | 66.4 MHz to 133.6 MHz  |                                |                          |
| MB88151APNF-G-500-JNERE1<br>MB88151APNF-G-501-JNERE1 |                       | Multiply-by-1/2  | 8.3 MHz to 16.7 MHz    |                                |                          |
| MB88151APNF-G-800-JNERE1<br>MB88151APNF-G-801-JNERE1 | 8.3 MHz to 16.7 MHz   | Multiply-by-8    | 66.4 MHz to 133.6 MHz  |                                |                          |

## ■ PACKAGE DIMENSION

|                                                                                                                                                                         |                                |               |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------|---------------|
| <div style="text-align: center;"> <p>8-pin plastic SOP</p>  <p>(FPT-8P-M02)</p> </div> | Lead pitch                     | 1.27 mm       |
|                                                                                                                                                                         | Package width × package length | 3.9 × 5.05 mm |
|                                                                                                                                                                         | Lead shape                     | Gullwing      |
|                                                                                                                                                                         | Sealing method                 | Plastic mold  |
|                                                                                                                                                                         | Mounting height                | 1.75 mm MAX   |
|                                                                                                                                                                         | Weight                         | 0.06 g        |
|                                                                                                                                                                         |                                |               |



Please confirm the latest Package dimension by following URL.  
<http://edevic.fujitsu.com/fj/DATASHEET/ef-ovpkiv.html>

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