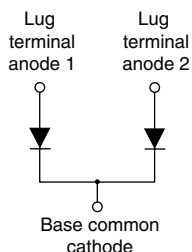


Schottky Rectifier, 400 A



TO-244



FEATURES

- 175 °C T_J operation
- Center tap module
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free
- Designed and qualified for industrial level



DESCRIPTION

The 401CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, converters, freewheeling diodes, welding and reverse battery protection.

PRODUCT SUMMARY

$I_{F(AV)}$	400 A
V_R	40/45 V

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{F(AV)}$	Rectangular waveform	400	A
V_{RRM}	Range	40/45	V
I_{FSM}	$t_p = 5 \mu s$ sine	25 000	A
V_F	200 Apk, $T_J = 125^\circ C$ (per leg)	0.56	V
T_J	Range	- 55 to 175	$^\circ C$

VOLTAGE RATINGS

PARAMETER	SYMBOL	401CNQ040PbF	401CNQ045PbF	UNITS
Maximum DC reverse voltage	V_R	40	45	V
Maximum working peak reverse voltage	V_{RWM}			

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current per leg See fig. 5 per device	$I_{F(AV)}$	50 % duty cycle at $T_C = 147^\circ C$, rectangular waveform		200 400	A
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I_{FSM}	5 μs sine or 3 μs rect. pulse 10 ms sine or 6 ms rect. pulse	Following any rated load condition and with rated V_{RRM} applied	25 000 3450	
Non-repetitive avalanche energy per leg	E_{AS}	$T_J = 25^\circ C$, $I_{AS} = 24$ A, $L = 1$ mH		270	mJ
Repetitive avalanche current per leg	I_{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		40	A

ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop per leg See fig. 1	$V_{FM}^{(1)}$	200 A	$T_J = 25\text{ }^{\circ}\text{C}$	0.67	V	
		400 A		0.78		
		200 A	$T_J = T_J \text{ maximum}$	0.56		
		400 A		0.69		
Maximum reverse leakage current per leg See fig. 2	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	20	mA	
		$T_J = 125\text{ }^{\circ}\text{C}$		180		
Maximum junction capacitance per leg	C_T	$V_R = 5\text{ }V_{DC}$ (test signal range 100 kHz to 1 MHz) $25\text{ }^{\circ}\text{C}$		10 300	pF	
Typical series inductance per leg	L_S	From top of terminal hole to mounting plane		5.0	nH	
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	V/ μ s	

Note(1) Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}	- 55	-	175	$^{\circ}\text{C}$
Thermal resistance, junction to case per leg	R_{thJC}	-	-	0.19	$^{\circ}\text{C/W}$
Thermal resistance, junction to case per module		-	-	0.095	
Thermal resistance, case to heatsink	R_{thCS}	-	0.10	-	
Weight		-	68	-	g
		-	2.4	-	oz.
Mounting torque		35.4 (4)		53.1 (6)	lbf · in (N · m)
Mounting torque center hole		30 (3.4)		40 (4.6)	
Terminal torque		30 (3.4)	-	44.2 (5)	
Vertical pull		-	-	80	lbf · in
2" lever pull		-	-	35	

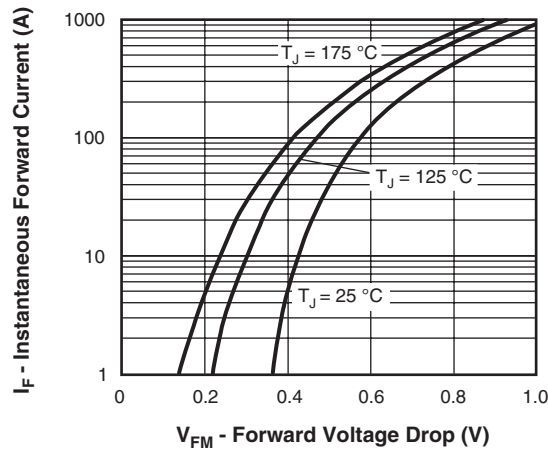


Fig. 1 - Maximum Forward Voltage Drop Characteristics

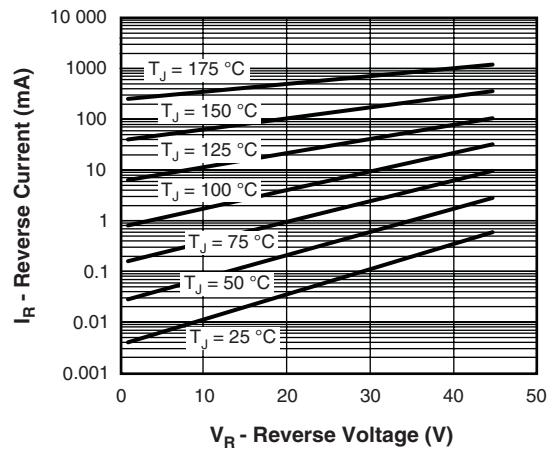


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

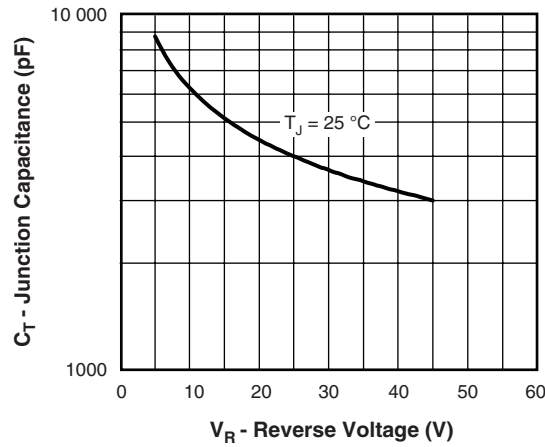


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

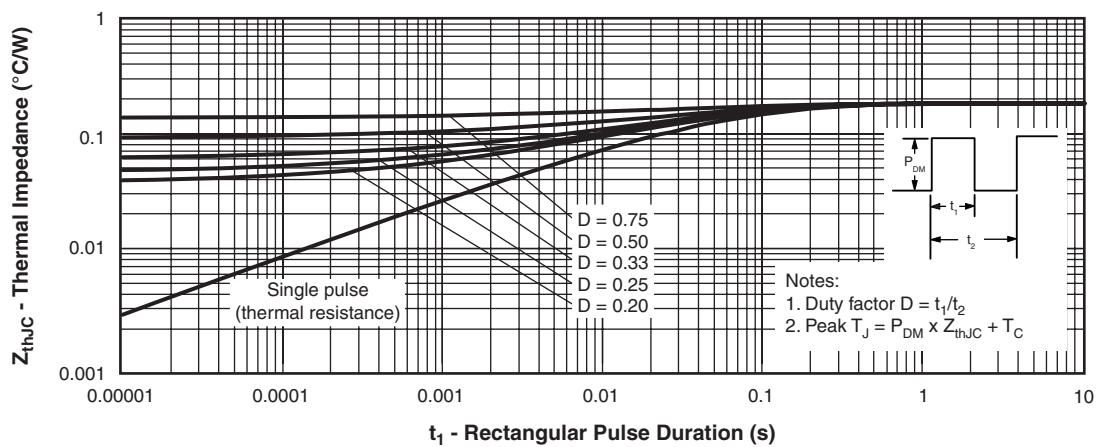


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

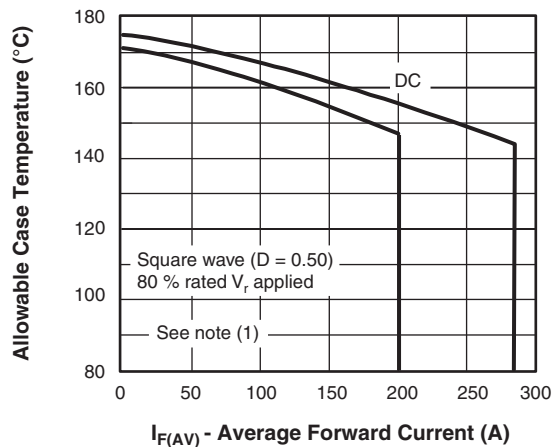


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

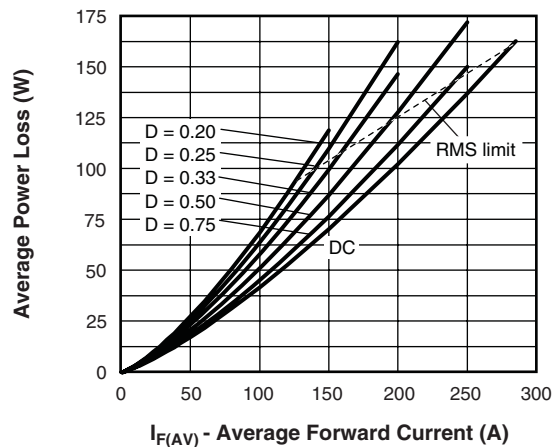


Fig. 6 - Forward Power Loss Characteristics

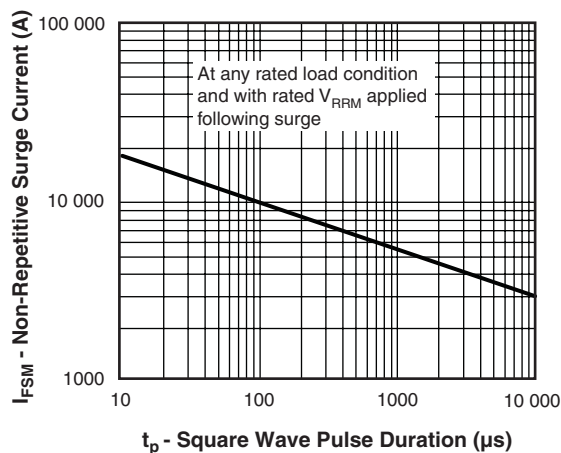


Fig. 7 - Maximum Non-Repetitive Surge Current

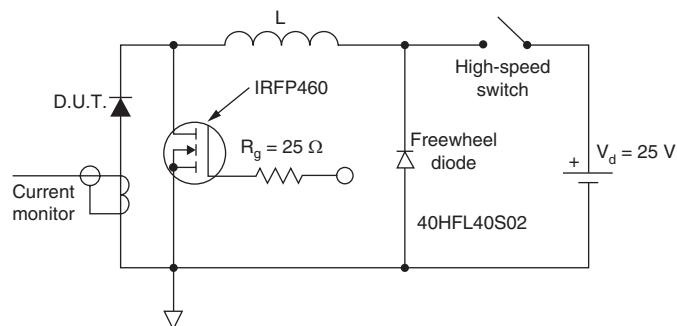


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R



ORDERING INFORMATION TABLE

Device code	40	1	C	N	Q	045	PbF
	1	2	3	4	5	6	7
1	-	Average current rating (x 10)					
2	-	Product silicon identification					
3	-	C = Circuit configuration					
4	-	N = Not isolated					
5	-	Q = Schottky rectifier diode					
6	-	Voltage ratings					
7	-	Lead (Pb)-free					

040 = 40 V
045 = 45 V

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95021

Disclaimer

All product specifications and data are subject to change without notice.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

Vishay disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless otherwise expressly indicated. Customers using or selling Vishay products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Vishay for any damages arising or resulting from such use or sale. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

Product names and markings noted herein may be trademarks of their respective owners.